

# Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((flag or bit or indicator or status) with ((reconfigur&6 or manage&4 or reconfigur&6) near3 (program\$1 or subroutine\$1 or instruction\$1))). clm.	US-PGPUB	OR	OFF	2005/09/27 16:08
L2	0	((flag or bit or indicator or status) and ((reconfigur&6 or manage&4 or reconfigur&6) near3 (program\$1 or subroutine\$1 or instruction\$1))). clm.	US-PGPUB	OR	OFF	2005/09/27 16:09
L3	0	((flag or bit or indicator or status) and (((reconfigur&6 or manage&4 or reconfigur&6) and (program\$1 or subroutine\$1 or instruction\$1))). clm.	US-PGPUB	OR	OFF	2005/09/27 16:09
L4	0	((((reconfigur&6 or manage&4 or reconfigur&6) and (program\$1 or subroutine\$1 or instruction\$1))). clm.	US-PGPUB	OR	OFF	2005/09/27 16:10
L5	0	((((reconfigur&6 or manage&4 or reconfigur&6 or structr\$2) and (program\$1 or subroutine\$1 or instruction\$1))).clm.	US-PGPUB	OR	OFF	2005/09/27 16:10

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	60	flash with random\$2 with byte	USPAT	OR	OFF	2005/09/27 09:58
S2	3	(flash with random\$2 with byte) with page	USPAT	OR	OFF	2005/09/27 10:04
S3	37	flash same page same data same redundant	USPAT	OR	OFF	2005/09/27 10:05
S4	6	flash same page same data same (redundant near5 (flag or identification or identifier or ID))	USPAT	OR	OFF	2005/09/27 10:46
S5	1	flash same (redundant near5 (flag or identification or identifier or ID) near5 program)	USPAT	OR	OFF	2005/09/27 10:17
S6	1	flash same (redundant near5 (flag or identification or identifier or ID)) same (read near3 program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:26
S7	392	flash same (boot near5 ROM) same program	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:29
S8	0	(flash with (device adj driver)) same (page with (id or identification or identifier or flag))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:33
S9	907	(711/103).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 10:32
S10	1289	(711/154).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 10:32
S11	551	(711/156).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 10:32
S12	15	S7 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:33
S13	7	S7 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:33

S14	0	S7 and S11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:37
S15	4598	(program or (device adj driver)) with (nonvolatile or flash) with (RAM or (random adj access adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:42
S16	182	S7 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:42
S17	42	flash same page same (redundant near5 (flag or identification or identifier or ID))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:46
S18	0	flash same page same (redundant near5 (flag or identification or identifier or ID)) same sequential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:47
S19	0	flash same (redundant near5 (flag or identification or identifier or ID)) same (page near10 sequential)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:48
S20	0	flash same (redundant near5 (flag or identification or identifier or ID)) same (sequential)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:49
S21	80	flash same (redundant near5 (flag or identification or identifier or ID))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:49

S22	0	flash same (redundant near5 (flag or identification or identifier or ID)) same (structural near3 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:50
S23	10	flash same (redundant near5 (flag or identification or identifier or ID)) same (unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:55
S24	42	flash same page same (redundant near5 (flag or identification or identifier or ID))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:57
S25	130	flash same page same (redundant)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:55
S26	0	flash same page same (redundant near5 (flag or identification or identifier or ID) with indiat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:56
S27	0	flash same (redundant near5 (flag or identification or identifier or ID) with indiat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:56
S28	80	flash same (redundant near5 (flag or identification or identifier or ID))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:57
S29	7	S15 and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 10:59

S30	1090821	(flash or nonvolatile)same page same configur\$3 (program or (device adj driver))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:01
S31	8	(flash or nonvolatile)same page same (configur\$3 near5 (program or (device adj driver)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:01
S32	1608	(flag or bit or indicator or status) with (((configur\$6 or manage\$4 or reconfigur\$6) near3 (program\$1 or suroutine\$1 or instruction\$1))	USPAT	OR	OFF	2005/09/27 12:23
S33	3408	(flag or bit or indicator or status) with (((configur\$6 or manage\$4 or reconfigur\$6) near3 (program\$1 or suroutine\$1 or instruction\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:13
S34	197324	(load\$3 or read\$3) with ((main adj memory) or RAM or DRAM or (volatile near1 memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:24
S35	71	S33 same S34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:45
S36	42	S35 and @PD<="20010316"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:13
S37	907	(711/103).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:46
S38	1289	(711/154).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:46
S39	551	(711/156).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:46
S40	561	(365/185.09).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:56
S41	1177	(365/185.33).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:58

S42	74	(365/235).CCLS.	USPAT; USOCR	OR	OFF	2005/09/27 12:58
S43	12	S33 and S37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S44	3	S33 and S38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S45	7	S33 and S39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S46	2	S33 and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S47	3	S33 and S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S48	0	S33 and S42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 12:59
S49	38	page with (flag or bit or indicator or status) with ((configur\$6 or manage\$4 or reconfigur\$6) near3 (program\$1 or suroutine\$1 or instruction\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:13
S50	15	S49 and @PD<="20010316"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:13

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 Terms used **flag configuration program subroutine instruction**

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### 1 [Truth in SPEC benchmarks](#)

Nikki Mirghafori, Margret Jacoby, David Patterson

 December 1995 **ACM SIGARCH Computer Architecture News**, Volume 23 Issue 5

 Full text available: ☒ pdf(801.93 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

The System Performance Evaluation Cooperative (SPEC) benchmarks are a set of integer and floating-point programs that are intended to be "effective and fair in comparing the performance of high performance computing systems". SPEC ratings are often quoted in company advertising and have been trusted as the de facto measure of comparison for computer systems. Recently, there has been some concern regarding the fairness and the value of these benc ...

**Keywords:** SPEC benchmarks, compiler-flag tuning, optimization, reproducibility

### 2 [The effect of compiler-flag tuning on SPEC benchmark performance](#)

Yin Chan, Ashok Sudarsanam, Andrew Wolfe

 September 1994 **ACM SIGARCH Computer Architecture News**, Volume 22 Issue 4

 Full text available: ☒ pdf(1.03 MB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

The SPEC CINT92 and CFP92 benchmark suites are application-based system benchmarks primarily intended for workstation-class system performance measurements. The SPEC CPU benchmark results are widely disseminated by system vendors and as such have become the de-facto standard for comparing system performance. Recently, many observers have expressed concerns about the suitability of published SPEC benchmark results in representing application performance on typical systems. The most outspoken conc ...

### 3 [CASE tool integration and adoption: Defect tracking and reliability modeling for a new product release](#)

Shanker Sanyal, Ken Aida, Kostas Gaitanos, George Wowk, Sam Lahiri

 November 1992 **Proceedings of the 1992 conference of the Centre for Advanced Studies on Collaborative research - Volume 1**

 Full text available: ☒ pdf(495.41 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

One of the indicators of success in the area of delivering quality software is the number of defects reported by the customer following product ship. Consequently, this necessitates a process for tracking defects in the development and testing phases. The consequent to defect arrival patterns can then be used to provide flags requiring changes in the

development process, and to project the likely number of customer reported problems once the product goes out of the door. This report describes the ...

4 Advances in system specification and system design frameworks: Codesign-extended applications 

Brian Grattan, Greg Stitt, Frank Vahid

May 2002 **Proceedings of the tenth international symposium on Hardware/software codesign**

Full text available:  pdf(544.66 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We challenge the widespread assumption that an embedded system's functionality can be captured in a single specification and then partitioned among software and custom hardware processors. The specification of some functions in software is very different from the specification of the same function in hardware - too different to conceive of automatically deriving one from the other. We illustrate this concept using a digital camera example. We introduce the idea of codesign-extended applications ...

**Keywords:** configurable logic, hardware/software cospecification, hardware/software partitioning, platform-based design, system-on-a-chip

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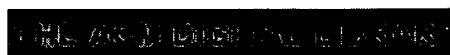
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### Quick Tips

- Enter your search terms in lower case with a space between the terms.

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Where are the sales offices?

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John Colter, Netscape Navigator

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"museum of natural history" "museum of modern art"

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## » Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

## Select Article Information

- ☐ 1. **Behavioral synthesis of fault secure controller/datapaths based on alias analysis**  
Lakshminarayana, G.; Raghunathan, A.; Jha, N.K.;  
Computers, IEEE Transactions on  
Volume 49, Issue 9, Sept. 2000 Page(s):865 - 885  
Digital Object Identifier 10.1109/12.869319  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(532 KB) IEEE JNL
- ☐ 2. **Multirate subband coding applied to digital speech interpolation**  
Derby, J.; Galand, C.;  
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig  
IEEE Transactions on  
Volume 35, Issue 12, Dec 1987 Page(s):1684 - 1698  
[AbstractPlus](#) | Full Text: [PDF](#)(1672 KB) IEEE JNL
- ☐ 3. **Large-Capacity Magnetic-Drum Memory System for an Electronic Switch**  
Kawamata, A.; Tominaga, H.; Onose, K.; Arai, Y.;  
Communications, IEEE Transactions on [legacy, pre - 1988]  
Volume 20, Issue 4, Aug 1972 Page(s):768 - 774  
[AbstractPlus](#) | Full Text: [PDF](#)(760 KB) IEEE JNL
- ☐ 4. **An Experimental Service for Adaptable Data Reconfiguration**  
Cerf, V.; Harslem, E.; Heafner, J.; Metcalfe, R.; White, J.;  
Communications, IEEE Transactions on [legacy, pre - 1988]  
Volume 20, Issue 3, Part 2, Jun 1972 Page(s):557 - 564  
[AbstractPlus](#) | Full Text: [PDF](#)(888 KB) IEEE JNL
- ☐ 5. **One-Way Multiaddress Satellite Data Communication System**  
Samejima, S.; Inoue, T.; Kagoshima, K.; Inoue, M.; Suzuki, M.;  
Selected Areas in Communications, IEEE Journal on  
Volume 1, Issue 1, Jan 1983 Page(s):118 - 125  
[AbstractPlus](#) | Full Text: [PDF](#)(1088 KB) IEEE JNL
- ☐ 6. **Parallel arrays of digital signal processors as central decision elements for triggers in high energy physics experiments**  
Crosetto, D.; Menichetti, E.; Rinaudo, G.; Werbrouck, A.E.;  
Nuclear Science, IEEE Transactions on

Volume 35, Issue 1, Part 1-2, Feb 1988 Page(s):248 - 252

Digital Object Identifier 10.1109/23.12717

[AbstractPlus](#) | Full Text: [PDF\(380 KB\)](#) IEEE JNL

☐ **7. An interactive distribution load forecasting methodology for minicomputers upon a Markov-type process**

Smolleck, H.A.; Kim, K.C.;

Power Systems, IEEE Transactions on

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